

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) An integrated circuit comprising a network and a plurality of electronic modules, said electronic modules being arranged to communicate to each other via the network, wherein the network is arranged to establish transactions between a first electronic module and at least two second electronic modules, ~~characterized in that~~wherein the network comprises means for replicating a single request from the first electronic module into at least two replicated requests, and for sending the replicated requests to the second electronic modules, wherein said means for replicating comprises an address space and a facility for mapping at least one multicast address onto at least two further addresses in a range of addresses, wherein the range of addresses comprises a first subset range of addresses and a second subset range of addresses, wherein each of the first and second subset ranges of addresses is greater than one address, wherein a first one of the second electronic modules is associated with the first subset range of addresses such that any request sent to any of the first subset range of address is sent to the first one of the second electronic modules and a second one of the second electronic modules is associated with the second subset range of addresses such that any

request sent to any of the second subset range of address is sent to the second one of the second electronic modules.

2. (Canceled)

3. (Previously presented) The integrated circuit as claimed in claim 1, wherein the means for replicating further comprises a facility for mapping at least one first multicast address onto at least one second multicast address, provided that the second multicast address is not mapped onto the first multicast address.

4. (Currently amended) The integrated circuit as claimed in claim 1, wherein the means for replicating further comprises a facility for mapping a range of multicast addresses onto at least ~~two ranges~~ of further the first and second subset ranges of addresses.

5. (Currently amended) The integrated circuit as claimed in claim 1, wherein the single request comprises a connection identifier for identifying a multicast connection, wherein the multicast connection includes at least one of guaranteed throughput, latency and jitter, ordered delivery, and flow control.

6. (Previously presented) The integrated circuit as claimed in claim 1, wherein means for replicating comprises a network interface circuit for performing the replication of the single request into

the replicated requests, and wherein the network interface circuit sends the replicated requests to the second electronic modules.

7. (Currently amended) A method for sending requests in an integrated circuit comprising a network and a plurality of electronic modules, which communicate to each other via the network, wherein the network establishes transactions between a first electronic module and at least two second electronic modules, characterized in that the method comprises the network replicating a single request from the first electronic module into at least two replicated requests, and the network sending the replicated requests to the second electronic modules, wherein said replicating comprises an address space and mapping at least one multicast address onto at least two further addresses in a range of addresses, wherein the range of addresses comprises a first subset range of addresses and a second subset range of addresses, wherein each of the first and second subset ranges of addresses is greater than one address, wherein a first one of the second electronic modules is associated with the first subset range of addresses such that any request sent to any of the first subset range of address is sent to the first one of the second electronic modules and a second one of the second electronic modules is associated with the second subset range of addresses such that any request sent to any of the second subset range of address is sent to the second one of the second electronic modules.

8. (New) The integrated circuit as claimed in claim 1, wherein the means for replicating further comprises a facility for mapping at least one first multicast address onto two or more addresses associated with a single one of the second electronic modules.